



PATENT
81788.0261

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Go Iwasaki

Serial No: 10/721,310

Confirmation No.: 7084

Filed: November 25, 2003

For: OUTPUT BUFFER CIRCUIT AND
SEMICONDUCTOR MEMORY USING THE
SAME

Art Unit: 2814

Examiner: Anh D. Mai

I hereby certify that this correspondence
is being deposited with the United States
Postal Service with sufficient postage as
first class mail in an envelope addressed
to: Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450, on
November 20, 2006
Date of Deposit
William H. Wright, Reg. No. 36,312
Name
[Signature] 11/20/06
Signature Date

PETITION FOR EXTENSION OF TIME

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with 37 C.F.R. 1.136, Applicant respectfully petitions the
Commissioner for a one-month extension of time extending from October 20, 2006 to
November 20, 2006, the period for response from the office action dated July 20,
2006. Enclosed is a check in the amount of \$120.00 for this extension.

Please charge any insufficiency or credit any overpayment to Deposit Account
No. 50-1314. A copy of this petition is enclosed.

Respectfully submitted,

HOGAN & HARTSON L.L.P.

By: [Signature]
William H. Wright
Registration No. 36,312
Attorney for Applicant(s)

Date: November 20, 2006

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